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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

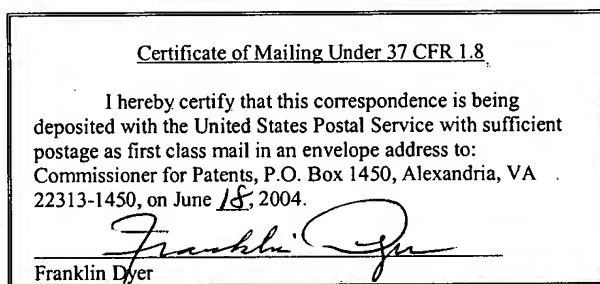
Applicant(s): Jeffrey Lutze et al.
Title: Self Aligned Non-Volatile Memory Cells And Processes For Fabrication
Application No.: 10/799,060 Filing Date: March 12, 2004
Examiner: Not yet assigned Group Art Unit: 2818
Docket No.: SNDK.352US0 Conf. No.: 6740

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

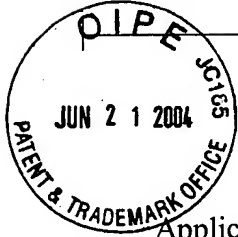
Dear Sir:

Formal drawings consisting of Figures 1-14B in the above-named application are hereby submitted. If there are any questions regarding these drawings, please call the undersigned attorney at (415) 318-1163.



Respectfully submitted,

Gerald P. Parsons 6/18/04
Gerald P. Parsons Date
Reg. No. 24,486



655 Montgomery Street, Suite 1800 San Francisco, Ca 94111
tel 415.318.1160 fax 415.693.0194

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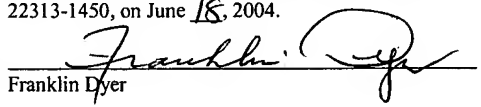
Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

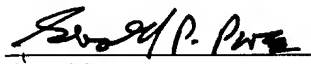
- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (1 page);
- (3) Submission of Formal Drawings (1 page);
- (4) Formal Drawings (14 sheets).

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being
deposited with the United States Postal Service with sufficient
postage as first class mail in an envelope address to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA
22313-1450, on June 18, 2004.


Franklin Dyer

Respectfully submitted,

 6/18/04
Gerald P. Parsons Date
Reg. No. 24,486